

Device Identity

UserDeviceName

52C1_F.PANGUE

<u>Overcurrent Settings</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase:						
PhsTripBlk	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
FastTripBlok	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
TCCPMinTrip	130	100	100	100	100	100
TCC1PCurve	133	104	104	104	104	104
TCC1PMultEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PMult	2	1	1	1	1	1
TCC1PAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PAdd	0	0	0	0	0	0
TCC1PMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC1PHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PHCT Mul	1.2	32	32	32	32	32
TCC1PHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
TCC2PCurve	133	117	117	117	117	117
TCC2PMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PMult	1	1	1	1	1	1
TCC2PAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PAdd	0	0	0	0	0	0
TCC2PMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC2PHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PHCT Mul	32	32	32	32	32	32
TCC2PHCTDly	0.016	0.016	0.016	0.016	0.016	0.016

Ground:						
GndTripBk	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
FastTripBlock	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
TCCGMinTrip	30	50	50	50	50	50
TCC1GCurve	140	106	106	106	106	106
TCC1GMultEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GMult	0.7	1	1	1	1	1
TCC1GAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GAdd	0	0	0	0	0	0
TCC1GMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC1GHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GHCT Mul	32	32	32	32	32	32
TCC1GHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
TCC2GCurve	140	135	135	135	135	135
TCC2GMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GMult	1	1	1	1	1	1
TCC2GAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GAdd	0	0	0	0	0	0
TCC2GMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC2GHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GHCT Mul	32	32	32	32	32	32
TCC2GHCTDly	0.016	0.016	0.016	0.016	0.016	0.016

Negative Sequence:						
NegSeqTripBlk	Blocked	Blocked	Blocked	Blocked	Blocked	Blocked
FastTripBlock	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
TCCQMinTrip	100	100	100	100	100	100
TCC1QCurve	104	104	104	104	104	104
TCC1QMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QMult	1	1	1	1	1	1
TCC1QAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QAdd	0	0	0	0	0	0
TCC1QMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC1QHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QHCTMul	32	32	32	32	32	32
TCC1QHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
TCC2QCurve	117	117	117	117	117	117
TCC2QMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QMult	1	1	1	1	1	1
TCC2QAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QAdd	0	0	0	0	0	0
TCC2QMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC2QHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QHCTMul	32	32	32	32	32	32
TCC2QHCTDly	0.016	0.016	0.016	0.016	0.016	0.016

<u>Operations Sequence</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Operations To LO	1	4	4	4	4	4
Phase/Neg Sequence:						
PQOper#1	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
PQOper#2	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
PQOper#3	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2
PQOper#4	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2
Ground:						
GndOper#1	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
GndOper#2	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
GndOper#3	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2
GndOper#4	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2

<u>Reclose Intervals</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase/Neg Sequence:						
PQOpenInt#1	2	2	2	2	2	2
PQOpenInt#2	2	2	2	2	2	2
PQOpenInt#3	5	5	5	5	5	5
Ground:						
GndOpenInt#1	2	2	2	2	2	2
GndOpenInt#2	2	2	2	2	2	2
GndOpenInt#3	5	5	5	5	5	5
ResetTime	3	30	30	30	30	30

<u>Cold Load Pickup</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
CLPUBlock	Blocked	Blocked	Blocked	Blocked	Blocked	Blocked
CLPUNumOps	2	2	2	2	2	2
CLPUOpenInt	2	2	2	2	2	2
CLPUActTime	20	20	20	20	20	20
Phase:						
CLPUPMinTrip	200	200	200	200	200	200
CLPUPCurve	117	117	117	117	117	117
CLPUPMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUPMult	1	1	1	1	1	1
CLPUPAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUPAdd	0	0	0	0	0	0
CLPUPMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUPMRTA	0.013	0.013	0.013	0.013	0.013	0.013
CLPUPHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUPHCT Mul	32	32	32	32	32	32
CLPUPHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
Ground:						
CLPUGMinTrip	100	100	100	100	100	100
CLPUGCurve	135	135	135	135	135	135
CLPUGMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUGMult	1	1	1	1	1	1
CLPUGAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUGAdd	0	0	0	0	0	0
CLPUGMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUGMRTA	0.013	0.013	0.013	0.013	0.013	0.013
CLPUGHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUGHCT Mul	32	32	32	32	32	32
CLPUGHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
Negative Sequence:						
CLPUQMinTrip	100	100	100	100	100	100
CLPUQCurve	101	101	101	101	101	101
CLPUQMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQMult	1	1	1	1	1	1
CLPUQAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQAdd	0	0	0	0	0	0
CLPUQMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQMRTA	0.013	0.013	0.013	0.013	0.013	0.013
CLPUQHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQHCT Mul	32	32	32	32	32	32
CLPUQHCTDly	0.016	0.016	0.016	0.016	0.016	0.016

<u>High Current Lockout</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase:						
HCLPPU	20000	20000	20000	20000	20000	20000
HCLPOp1Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
HCLPOp2Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
HCLPOp3Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
Ground:						
HCLGPU	20000	20000	20000	20000	20000	20000
HCLGOp1Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
HCLGOp2Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
HCLGOp3Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
Negative Sequence:						
HCLQPU	20000	20000	20000	20000	20000	20000
HCLQOp1Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
HCLQOp2Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
HCLQOp3Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
Frequency	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Underfrequency:						
UFreqEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
UFreq1PU	56	56	56	56	56	56
UFreq1Time	100	100	100	100	100	100
UFreq2PU	56	56	56	56	56	56
UFreq2Time	100	100	100	100	100	100
Overfrequency:						
OFreqEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
OFreq1PU	64	64	64	64	64	64
OFreq1Time	100	100	100	100	100	100
OFreq2PU	64	64	64	64	64	64
OFreq2Time	100	100	100	100	100	100
U/OF Tripping Supervision:						
Freq:MinVolt	3.6	3.6	3.6	3.6	3.6	3.6
UF Loadshed Restore:						
FreqRestoreEnable	Off	Off	Off	Off	Off	Off
Freq:81OR:PU	60.05	60.05	60.05	60.05	60.05	60.05
Freq:62Schedule	300	300	300	300	300	300
Freq:62Abort	600	600	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off	Off	Off

<u>ReclsTime and Control</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
79ResetTarEnable	Enabled	Enabled	Enabled	Enabled	Enabled	Enabled
79ResetTar	2	2	2	2	2	2
79SeqCoorEnable	Enabled	Enabled	Enabled	Enabled	Enabled	Enabled
79SeqCoorOps	2	2	2	2	2	2

<u>Reclose Retry</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
RecloseRetryEnable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
RecloseRetryAttempts	1	1	1	1	1	1
RecloseRetryInterval	60	60	60	60	60	60

<u>Voltage</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Undervoltage:						
UVolt1PEnable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
UVolt3PEnable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
UVolt1P/3Pinhibit	Off	Off	Off	Off	Off	Off
UVolt1PPU	11.52	11.52	11.52	11.52	11.52	11.52
UVolt1PTime	100	100	100	100	100	100
UVolt3PPU	11.52	11.52	11.52	11.52	11.52	11.52
UVolt3PTime	100	100	100	100	100	100
Overvoltage:						
OVoltEnable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
OVolt1PPU	16.2	16.2	16.2	16.2	16.2	16.2
OVolt1PTime	100	100	100	100	100	100
OVolt3PPU	16.2	16.2	16.2	16.2	16.2	16.2
OVolt3PTime	100	100	100	100	100	100

U/OV Loadshed Restore:						
VoltRestoreEnable	Off	Off	Off	Off	Off	Off
VoltRestoreMode	Any1Phs	Any1Phs	Any1Phs	Any1Phs	Any1Phs	Any1Phs
VoltRestHiL	15.12	15.12	15.12	15.12	15.12	15.12
VoltRestLoL	13.68	13.68	13.68	13.68	13.68	13.68
Freq:62Schedule	300	300	300	300	300	300
Freq:62Abort	600	600	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off	Off	Off

<u>Sensitive Earth Fault</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
SEFBlock	Unblkcd	Blocked	Blocked	Blocked	Blocked	Blocked
SEFMinTrip	40	40	40	40	40	40
SEFTime	10	120	120	120	120	120
SEFRedInt	2	2	2	2	2	2
SEFNumOps	3	4	4	4	4	4

<u>Ungrounded SEF</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
USEFBlock	Blocked	Blocked	Blocked	Blocked	Blocked	Blocked
USEFMinTrip	40	40	40	40	40	40
USEFTime	120	120	120	120	120	120
USEFV0	0.48	0.48	0.48	0.48	0.48	0.48
USEFNumOps	4	4	4	4	4	4
USEFOplnt#1	2	2	2	2	2	2
USEFOplnt#2	2	2	2	2	2	2
USEFOplnt#3	5	5	5	5	5	5
DirUSEFMTA	60	60	60	60	60	60
DirUSEF	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional

<u>Directional Control</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 3</u>
DirMTA	60	60	60	60	60	60
DirPhs	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirGnd	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirNegSeq	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional

<u>Low Set</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase:						
LSPEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
LSPPU	3200	3200	3200	3200	3200	3200
LSPTmeDelay	100	100	100	100	100	100
Ground:						
LSGEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
LSGPU	1600	1600	1600	1600	1600	1600
LSGTmeDelay	100	100	100	100	100	100
Negative Sequence:						
LSQEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
LSQPU	3200	3200	3200	3200	3200	3200
LSQTmeDelay	100	100	100	100	100	100

<u>Sync Check</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
25DV:Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:DLDB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:DLHB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:HLDB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:HLHB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:27	15840	15840	15840	15840	15840	15840
25DV:27DEAD	2640	2640	2640	2640	2640	2640
25DV:59	12240	12240	12240	12240	12240	12240
25DV:59LME	11520	11520	11520	11520	11520	11520
25DV	40	40	40	40	40	40
StaticAngleDelay	10	10	10	10	10	10
MechanismOpDelay	0.1	0.1	0.1	0.1	0.1	0.1