

Device Identity

UserDeviceName

52C4_FAENAS_PA

<u>Overcurrent Settings</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase:						
PhsTripBlk	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
FastTripBlock	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
TCCPMinTrip	35	100	100	100	100	100
TCC1PCurve	IEC INV (200)	104	104	104	104	104
TCC1PMultEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PMult	0.1	1	1	1	1	1
TCC1PAddEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PAdd	0.1	0	0	0	0	0
TCC1PMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC1PHCTEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1PHCT Mul	28.57	32	32	32	32	32
TCC1PHCTDly	0.15	0.016	0.016	0.016	0.016	0.016
TCC2PCurve	IEC INV (200)	117	117	117	117	117
TCC2PMultEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PMult	0.1	1	1	1	1	1
TCC2PAddEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PAdd	0.1	0	0	0	0	0
TCC2PMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC2PHCTEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2PHCT Mul	28.57	32	32	32	32	32
TCC2PHCTDly	0.15	0.016	0.016	0.016	0.016	0.016

Ground:						
GndTripBlk	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
FastTripBlock	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
TCCGMinTrip	5	50	50	50	50	50
TCC1GCurve	IEC VI (201)	106	106	106	106	106
TCC1GMultEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GMult	0.3	1	1	1	1	1
TCC1GAddEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GAdd	0.1	0	0	0	0	0
TCC1GMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC1GHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1GHCT Mul	32	32	32	32	32	32
TCC1GHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
TCC2GCurve	IEC VI (201)	135	135	135	135	135
TCC2GMultEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GMult	0.3	1	1	1	1	1
TCC2GAddEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GAdd	0.1	0	0	0	0	0
TCC2GMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC2GHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2GHCT Mul	32	32	32	32	32	32
TCC2GHCTDly	0.016	0.016	0.016	0.016	0.016	0.016

Negative Sequence:						
NegSeqTripBk	Blocked	Blocked	Blocked	Blocked	Blocked	Blocked
FastTripBlock	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd	Unblckd
TCCQMinTrip	100	100	100	100	100	100
TCC1QCurve	104	104	104	104	104	104
TCC1QMltEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QMlt	1	1	1	1	1	1
TCC1QAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QAdd	0	0	0	0	0	0
TCC1QMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC1QHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC1QHCT Mul	32	32	32	32	32	32
TCC1QHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
TCC2QCurve	117	117	117	117	117	117
TCC2QMltEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QMlt	25	1	1	1	1	1
TCC2QAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QAdd	0	0	0	0	0	0
TCC2QMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QMRTA	0.013	0.013	0.013	0.013	0.013	0.013
TCC2QHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
TCC2QHCT Mul	32	32	32	32	32	32
TCC2QHCTDly	0.016	0.016	0.016	0.016	0.016	0.016

<u>Operations Sequence</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Operations To LO	1	4	4	4	4	4
Phase/Neg Sequence:						
PQOper#1	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
PQOper#2	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
PQOper#3	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2
PQOper#4	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2
Ground:						
GndOper#1	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
GndOper#2	TCC1	TCC1	TCC1	TCC1	TCC1	TCC1
GndOper#3	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2
GndOper#4	TCC2	TCC2	TCC2	TCC2	TCC2	TCC2

<u>Reclose Intervals</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase/Neg Sequence:						
PQOpenInt#1	2	2	2	2	2	2
PQOpenInt#2	2	2	2	2	2	2
PQOpenInt#3	5	5	5	5	5	5
Ground:						
GndOpenInt#1	5	2	2	2	2	2
GndOpenInt#2	5	2	2	2	2	2
GndOpenInt#3	5	5	5	5	5	5
ResetTime	5	30	30	30	30	30

<u>Cold Load Pickup</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
CLPUBlock	Blocked	Blocked	Blocked	Blocked	Blocked	Blocked
CLPU NumOps	1	2	2	2	2	2
CLPUOpenInt	2	2	2	2	2	2
CLPUActTime	20	20	20	20	20	20
Phase:						
CLPUPMinTrip	100	200	200	200	200	200
CLPUPCurve	Constant	117	117	117	117	117
CLPUPMultEnable	Enabled	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUPMult	0.2	1	1	1	1	1
CLPUAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUAdd	0	0	0	0	0	0
CLPUMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUMRTA	0.013	0.013	0.013	0.013	0.013	0.013
CLPUHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUHCT Mul	32	32	32	32	32	32
CLPUHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
Ground:						
CLPUGMinTrip	1600	100	100	100	100	100
CLPUGCurve	Constant	135	135	135	135	135
CLPUGMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUGMult	1	1	1	1	1	1
CLPUAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUAdd	0	0	0	0	0	0
CLPUMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUMRTA	0.013	0.013	0.013	0.013	0.013	0.013
CLPUHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUHCT Mul	32	32	32	32	32	32
CLPUHCTDly	0.016	0.016	0.016	0.016	0.016	0.016
Negative Sequence:						
CLPUQMinTrip	100	100	100	100	100	100
CLPUQCurve	101	101	101	101	101	101
CLPUQMultEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQMult	1	1	1	1	1	1
CLPUQAddEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQAdd	0	0	0	0	0	0
CLPUQMRTAEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQMRTA	0.013	0.013	0.013	0.013	0.013	0.013
CLPUQHCTEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
CLPUQHCT Mul	32	32	32	32	32	32
CLPUQHCTDly	0.016	0.016	0.016	0.016	0.016	0.016

<u>High Current Lockout</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase:						
HCLPPU	100	20000	20000	20000	20000	20000
HCLPOp1Enable	Enabled	Disabl	Disabl	Disabl	Disabl	Disabl
HCLPOp2Enable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
HCLPOp3Enable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
Ground:						
HCLGPU	100	20000	20000	20000	20000	20000
HCLGOp1Enable	Enabled	Disabl	Disabl	Disabl	Disabl	Disabl
HCLGOp2Enable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
HCLGOp3Enable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
Negative Sequence:						
HCLQPU	20000	20000	20000	20000	20000	20000
HCLQOp1Enable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
HCLQOp2Enable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
HCLQOp3Enable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
Frequency	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Underfrequency:						
UFreqEnable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
UFreq1PU	56	56	56	56	56	56
UFreq1Time	100	100	100	100	100	100
UFreq2PU	56	56	56	56	56	56
UFreq2Time	100	100	100	100	100	100
Overfrequency:						
OFreqEnable	Disabl	Disabl	Disabl	Disabl	Disabl	Disabl
OFreq1PU	64	64	64	64	64	64
OFreq1Time	100	100	100	100	100	100
OFreq2PU	64	64	64	64	64	64
OFreq2Time	100	100	100	100	100	100
U/OF Tripping Supervision:						
Freq:MinVolt	3.6	3.6	3.6	3.6	3.6	3.6
UF Loadshed Restore:						
FreqRestoreEnable	Off	Off	Off	Off	Off	Off
Freq:81OR:PU	60.05	60.05	60.05	60.05	60.05	60.05
Freq:62Schedule	300	300	300	300	300	300
Freq:62Abort	600	600	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off	Off	Off

<u>ReclsTime and Control</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
79ResetTarEnable	Enabled	Enabled	Enabled	Enabled	Enabled	Enabled
79ResetTar	2	2	2	2	2	2
79SeqCoorEnable	Disabld	Enabled	Enabled	Enabled	Enabled	Enabled
79SeqCoorOps	2	2	2	2	2	2

<u>Reclose Retry</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
RecloseRetryEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
RecloseRetryAttempts	1	1	1	1	1	1
RecloseRetryInterval	10	60	60	60	60	60

<u>Voltage</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Undervoltage:						
UVolt1PEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
UVolt3PEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
UVolt1P/3Pinhibit	Off	Off	Off	Off	Off	Off
UVolt1PPU	11.52	11.52	11.52	11.52	11.52	11.52
UVolt1PTime	100	100	100	100	100	100
UVolt3PPU	11.52	11.52	11.52	11.52	11.52	11.52
UVolt3PTime	100	100	100	100	100	100

Overvoltage:						
OVoltEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
OVolt1PPU	16.2	16.2	16.2	16.2	16.2	16.2
OVolt1PTime	100	100	100	100	100	100
OVolt3PPU	16.2	16.2	16.2	16.2	16.2	16.2
OVolt3PTime	100	100	100	100	100	100

U/OV Loadshed Restore:						
VoltRestoreEnable	Off	Off	Off	Off	Off	Off
VoltRestoreMode	Any1Phs	Any1Phs	Any1Phs	Any1Phs	Any1Phs	Any1Phs
VoltRestHiL	15.12	15.12	15.12	15.12	15.12	15.12
VoltRestLoL	13.68	13.68	13.68	13.68	13.68	13.68
Freq:62Schedule	300	300	300	300	300	300
Freq:62Abort	600	600	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off	Off	Off

<u>Sensitive Earth Fault</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
SEFBlock	Blocked	Blocked	Blocked	Blocked	Blocked	Blocked
SEFMinTrip	40	40	40	40	40	40
SEFTime	10	120	120	120	120	120
SEFReclnt	2	2	2	2	2	2
SEFNumOps	1	4	4	4	4	4

Ungrounded SEF

	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
USEFBlock	Blocked	Blocked	Blocked	Blocked	Blocked	Blocked
USEFMinTrip	40	40	40	40	40	40
USEFTime	10	120	120	120	120	120
USEFV0	0.48	0.48	0.48	0.48	0.48	0.48
USEFNumOps	1	4	4	4	4	4
USEFOpInt#1	2	2	2	2	2	2
USEFOpInt#2	2	2	2	2	2	2
USEFOpInt#3	5	5	5	5	5	5
DirUSEFMTA	60	60	60	60	60	60
DirUSEF	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional

Directional Control

	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 3</u>
DirMTA	60	60	60	60	60	60
DirPhs	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirGnd	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirNegSeq	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional	NonDirectional

Low Set

	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
Phase:						
LSPEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
LSPPU	3200	3200	3200	3200	3200	3200
LSPTIMEDelay	100	100	100	100	100	100
Ground:						
LSGEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
LSGPU	1600	1600	1600	1600	1600	1600
LSGTimeDelay	100	100	100	100	100	100
Negative Sequence:						
LSQEnable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
LSQPU	3200	3200	3200	3200	3200	3200
LSQTimeDelay	100	100	100	100	100	100

Sync Check

	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>	<u>Alternate 4</u>	<u>Alternate 5</u>
25DV:Enable	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:DLDB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:DLHB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:HLDB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:HLHB	Disabld	Disabld	Disabld	Disabld	Disabld	Disabld
25DV:27	15840	15840	15840	15840	15840	15840
25DV:27DEAD	2640	2640	2640	2640	2640	2640
25DV:59	12240	12240	12240	12240	12240	12240
25DV:59LVE	11520	11520	11520	11520	11520	11520
25DV	40	40	40	40	40	40
StaticAngleDelay	10	10	10	10	10	10
MechanismOpDelay	0.1	0.1	0.1	0.1	0.1	0.1